

FIG. 1

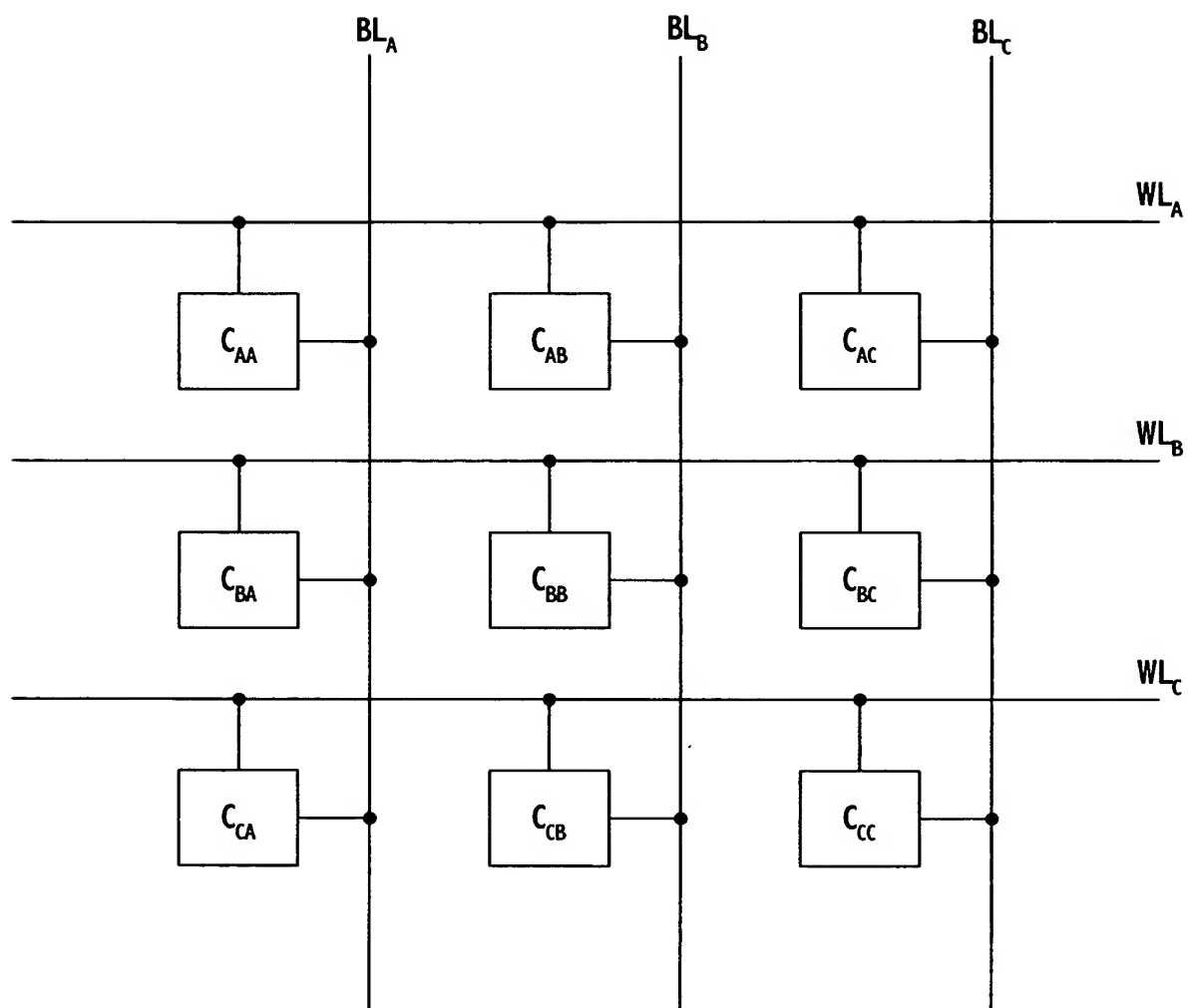


FIG. 2

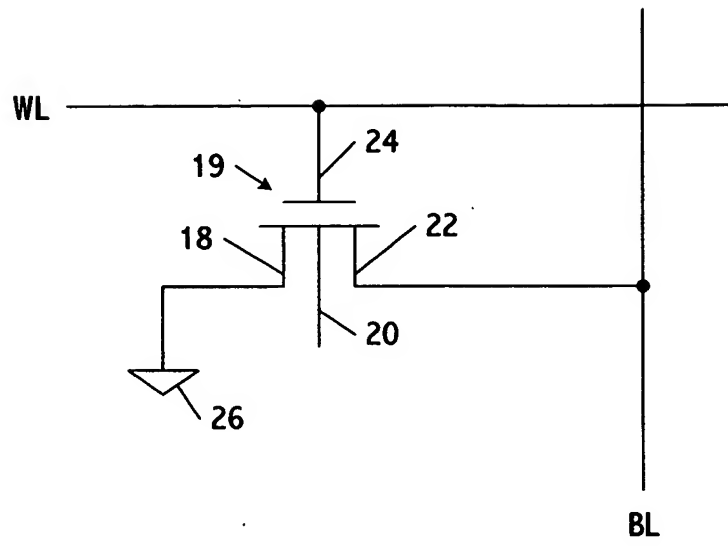


FIG. 3A

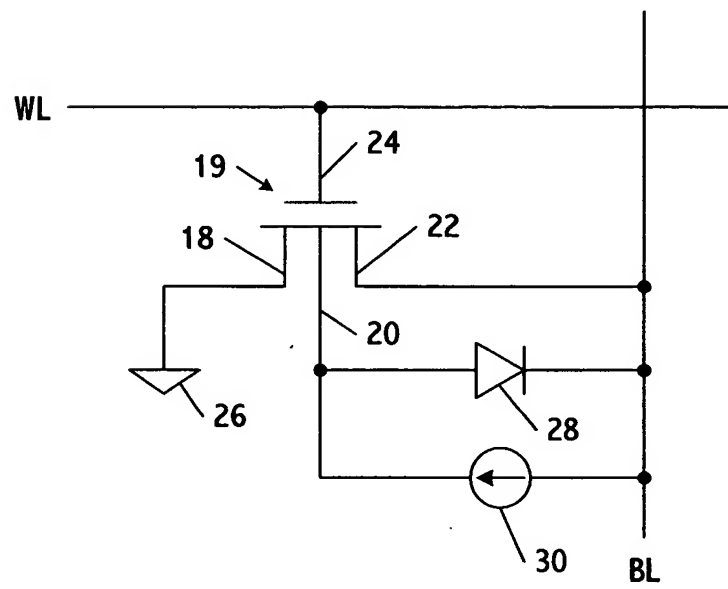


FIG. 3B

	SECOND CHANNEL INTERFACE	GATE (WL)	FIRST CHANNEL INTERFACE (BL)
HOLD	LOW	VERY LOW	VERY LOW, LOW, OR HIGH
WRITE LOGICAL 0	LOW	HIGH	VERY LOW
WRITE LOGICAL 1	LOW	HIGH	HIGH

FIG. 4

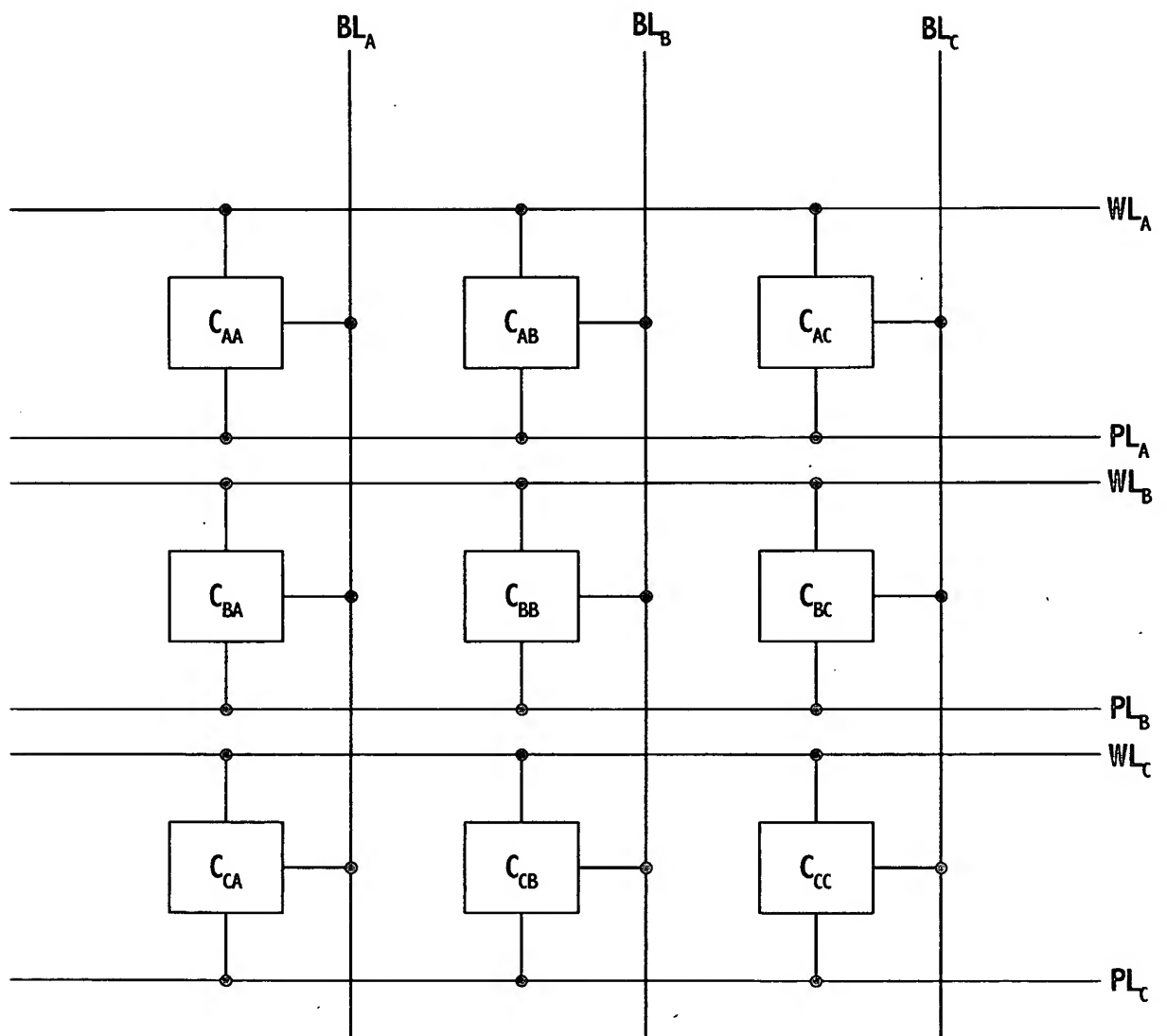


FIG. 5

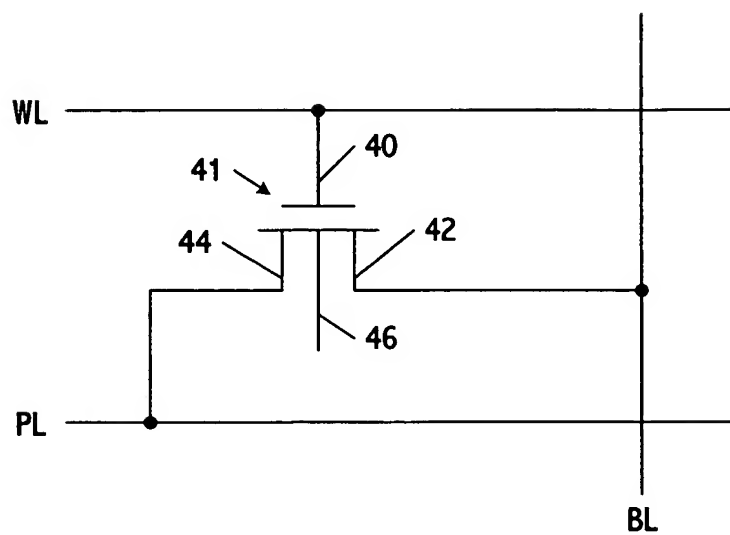


FIG. 6A

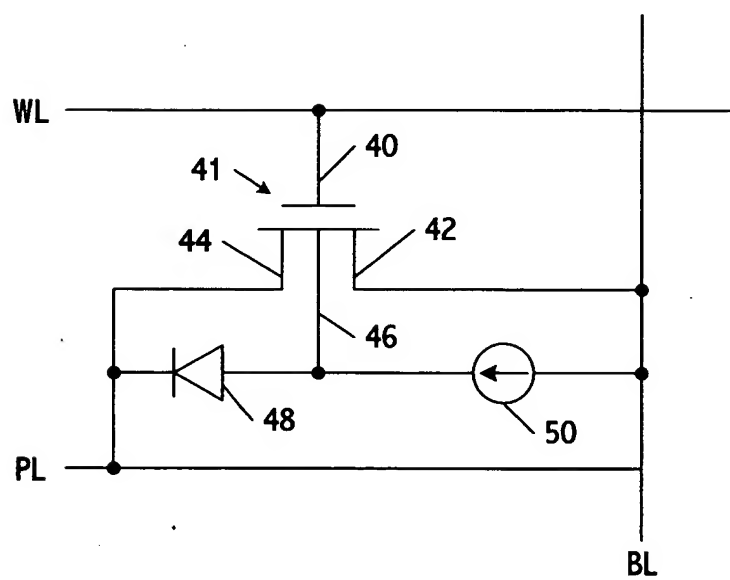


FIG. 6B

	SECOND CHANNEL INTERFACE (PURGE LINE)	GATE (WORD LINE)	FIRST CHANNEL INTERFACE (BIT LINE)
HOLD	LOW	LOW	LOW OR HIGH
PURGE	VERY LOW	VERY LOW	LOW
WRITE 0	LOW	HIGH	LOW
WRITE 1	LOW	HIGH	HIGH

FIG. 7